

Comparable to Form PTO-1449 (Rev. 5/92)		U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. SYN 1756		Serial No. 09/535,831	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant Ofek et al.		RECEIVED JUL 07 2000 GROUP 2700	
				Filing Date March 28, 2000		Group 2739	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
DL		5,418,779	5/23/95	Yemini et al.	370	54	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
DL		A. Pattavina, "Switching Theory: Architecture and Performance in Broadband ATM Networks", John Wiley & Sons, NY (1998), table of contents.					
DL		Nichols, et al. "Definition of the Differentiated Services Field (DS Field) in the IPv4 and Ipv6 Headers", Network Working Group Request for Comments 2474, December 1998, pp. 1-20					
DL		Kamiyama, et al., "Quasi-STM Transmission Method Based on ATM Network," IEEE GLOBECOM'94, 1994, pages 1808-1814					
DL		Mills, et al., "Final Report on the Highball Project," Technical Report 95-4-1, University of Delaware, April 1995					
DL		Awdeh, et al., "Survey of ATM Switch Architectures," Computer Networks and ISDN Systems, No. 27, 1995, pages 1567-1613					
DL		Broomell, et al., "Classification Categories and Historical Development of Switching Fabric Topologies," Computing Surveys, Vol. 15, No. 2, June 1983					
DL		Ahmadi, et al., "A Survey of Modern High-Performance Switching Techniques," IEEE Journal on Selected Areas in Communications, Vol. 7, No. 7, September 1989.					
DL		T. G. Robertazzi, Editor, "Performance Evaluation of High Speed Switching Fabrics and Networks," IEEE Press, 1992 table of contents					
DL		Goke, et al., "Banyan Networks for Partitioning Multiprocessor Systems," 1st Annual Symposium on Computer Architecture, December 1973, pages 21-28					
DL		Shiomoto, et al., "Dynamic Burst Transfer Time-Slot-Base Network," IEEE Communications Magazine, October 1999, pages 88-96					
DL		Bohm, et al., "The DTM Gigabit Network," Journal of High Speed Networks, Vol. 3, No. 2, 1994					
DL		Bohm, et al., "Fast Circuit Switching for the Next Generation of High Performance Networks," IEEE Journal on Selected Areas in Communications, Vol. 14, No. 2, pages 298-305, February 1996					
Examiner				Date Considered			
LEVITAN				5/30/03			
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)							
DL		Y. Ofek, "Integration Of Voice Communication On A Synchronous Optical Hypergraph", IEEE INFOCOM'88, 1988.					
DL	!	Li et al., "Time-Driven Priority: Flow Control For Real-Time Heterogeneous Internetworking", IEEE INFOCOM'96, 1996					
DL		Li et al., "Pseudo-Isochronous Cell Forwarding", IEEE INFOCOM'94, pp. 1-19; 1994;					
DL		A. R. Jacob, "A Survey of Fast Packet Switches", Computer Communications Review, January 1990, pages 54-64.					
DL		Y. Ofek, "The Topology, Algorithms And Analysis Of A Synchronous Optical Hypergraph Architecture", Ph.D. Dissertation, Electrical Engineering Department, University of Illinois at Urbana, Report No. UIUCDCS-R-87 1343, May 1987 PP. 84-154					
DL		A. G. Fraser, "Early Experiment with Asynchronous Time Division Networks", IEEE Networks, pp. 12-26, January 1993					
DL		A. Pattavina, "Non-blocking Architecture for ATM Switching", IEEE Communications Magazine, February 1993, pages 37-48					
DL		John C. Bellamy, "Digital Network Synchronization", IEEE Communications Magazine, April 1995, pages 70-83.					
DL		E. W. Zegura, "Architecture for ATM Switching Systems", IEEE Communications Magazine, February 1993, pages 28- 37					
DL		A. Tanenbaum, Computer Networks (3rd Ed.) Prentice Hall, 1996 Chapter 5					
Examiner		LEVITAN		Date Considered		5/30/03	
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INFORMATION DISCLOSURE CITATION (Use additional sheets if necessary)				Applicant Y. Ofek et al.		RECEIVED MAR 05 2001 Technology Center							
				Filing Date March 28, 2000									
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)													
DL		S.J.Golestani, "A Framing Strategy for Congestion Management", <u>IEEE Journal on Selected Areas in Communications</u> , Vol. 9, No. 7, September 1991, pp. 1064-1077.											
DL		Li et al., "Pseudo-isonchronous Cell Forwarding", <u>Computer Networks and ISDN Systems</u> , Vol. 30 (1998), pp. 2359-2372.											
DL		Li et al., "Time-driven Priority' Flow Control for Real-time Heterogeneous Internetworking", <u>Proceedings of Infocom</u> , Vol. 15, Los Alamitos, CA, March 1996, pp. 189-197.											
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